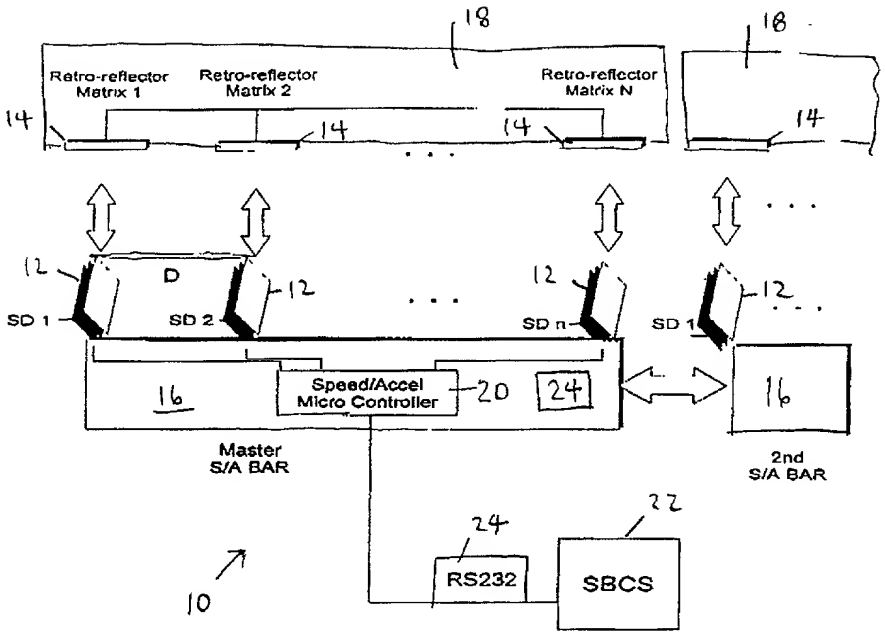
18
18
Retro-reflector Matrix 1
Retro-reflector Matrix 2
Retro-reflector Matrix N
14
14
14
14
12
D
12
12
12
SD 1
SD 2
SD n
SD 1
16
Speed/Accel Micro Controller
20
24
16
Master S/A BAR
2nd S/A BAR
22
24
RS232
SBCS
10

FIG. 1

PATENT APPLICATION

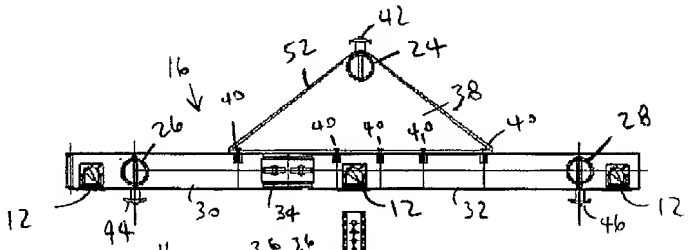

FIG. 2

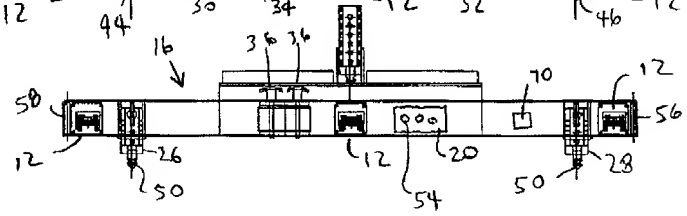

FIG. 3

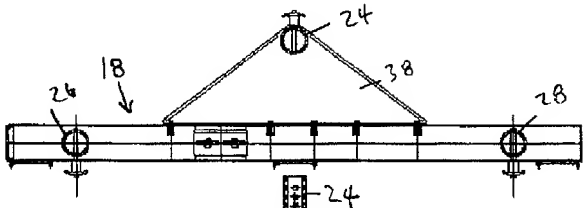

FIG. 4

FIG. 5

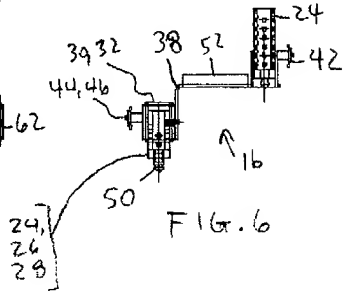

FIG. 6